

Docket No.: M4065.0761/P761

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Chandra Mouli, et al.

Application No.: 10/602,721

Filed: June 25, 2003

For: REDUCED BARRIER

PHOTODIODE/TRANSFER GATE DEVICE STRUCTURE FOR HIGH

EFFICIENCY CHARGE TRANSFER AND REDUCED LAG AND METHOD OF

**FORMATION** 

Confirmation No.: 9931

Art Unit: 2811

Examiner: Not Yet Assigned

## REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the aboveidentified patent application. The official Filing Receipt received by Applicants, a copy of which is attached hereto, has the following errors:

The title should read: Reduced Barrier Photodiode/Transfer Gate Device Structure For High Efficiency Charge Transfer And Reduced Lag And Method Of Formation; and

The attorney docket no. should read: M4065.0761/P761.

Application No.: 10/602,721 Docket No.: M4065.0761/P761

Applicants additionally request that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: November 24, 2003

Respectfully submitted,

Thomas J. D'Amico

Registration No.: 28,371

Elizabeth Parsons

Registration No.: 52,499

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

**Attorneys for Applicants** 

IND CLMS

5



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Most 1459 Alexandria, Viginia 22313-1450 www.unpto.gov

**FILING OR 371** DRAWINGS ART UNIT FIL FEE REC'D ATTY.DOCKET NO APPL NO. (c) DATE ~N4085:0761/P761~ 1602 10/602.721 06/25/2003 2811

M 4065.0761

58 CONFIRMATION NO. 9931

TOT CLMS

24998

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP

2101 L STREET NW

WASHINGTON, DC 20037-1526

\*OC000000011185081

NOV 10 PH2:21

Date Mailed: 11/05/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply t the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Chandra Mouli, Boise, ID; Howard E. Rhodes, Boise, ID;

Domestic Priority data as claimed by applicant

**Foreign Applications** 

If Required, Foreign Filing License Granted: 11/04/2003

Projected Publication Date: To Be Determined - pending completion of Corrected Papers

Non-Publication Request: No

Early Publication Request: No

**Title** 

TAANSFER

Reduced barrier photodiode/, gate device structure for high efficiency charge transfer and reduced lag and method of formation

**Preliminary Class**